Notice of References Cited Application/Control No. | Applicant(s)/Patent Under | Reexamination | KRAUS, RUDY Examiner | Art Unit | Nicholas Ponomarenko | 2834 | Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-6,583,519 B2	06-2003	Aberle et al.	307/10.1
*	В	US-6,463,738 B1	10-2002	Pinkerton et al.	60/646
*	С	US-6,396,725 B1	05-2002	Jacobs et al.	363/131
*	D	US-6,225,708 B1	05-2001	Furukawa et al.	307/66
*	E	US-6,157,168 A	12-2000	Malik, Randhir S.	320/128
*	F	US-5,894,412	04-1999	Faulk, Richard A.	363/17
*	G	US-5,844,329	12-1998	Bailey et al.	307/66
*	Н	US-5,784,626	07-1998	Odaohara, Shigefumi	713/300
*	ı	US-5,612,581 A	03-1997	Kageyama, Hironobu	307/64
*	J	US-5,247,205 A	09-1993	Mototani et al.	307/66
*	К	US-4,811,185	03-1989	Cook et al.	363/17
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	σ					
	R				-	
	s					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	IEEE Transactions on Power Electronics, Vol.17, No.6, Nov. 2002. "A Zero-Voltage and Zero-Current Switching Three-Level DC/DC Converter".				
	٧					
	w					
	х					

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.